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20277 7590 12/02/2003

MCDERMOTT WILL & EMERY  
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/964,480	09/28/2001	Keiichi Fujimoto	740819-039	2785

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT TESTING SYSTEM AND METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1330	\$300	\$1630	03/02/2004
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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PATEL, PARESH H	2829	324-765000
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1 McDermott, Will & Emery  
2 \_\_\_\_\_  
3 \_\_\_\_\_

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(A) NAME OF ASSIGNEE

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MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. Osaka, JP

Please check the appropriate assignee category or categories (will not be printed on the patent);  individual  corporation or other private group entity  government

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Michael J. Fogarty Reg. #36,139 2/26/04

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